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Web

Results 1 - 10 of about 1,880 for relocation instructions and disassembler. (0.45 seconds)

### Software Security Against Cracking

... Polymorphic Anti-Disassembler Code Between Real Instructions, Yes. Yes. Automatic Garbage Blocks Insertion, Yes. Yes. ... Random Variables **Relocation**, Yes. No. ...  
[www.xprotector.com/xprot.htm](http://www.xprotector.com/xprot.htm) - 29k - [Cached](#) - [Similar pages](#)

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Relocation Information  
 Information and resources concerning **relocation** programs.  
[www.commerce-database.com](http://www.commerce-database.com)

### Protools - Decompilers

... and Pentium 4 specific instructions. ... News: - Many changes in Resource Editor, **Disassembler**, Header Info Viewer ... Review of contents of the base **relocation** table. ...  
[protools.anticrack.de/decompilers.htm](http://protools.anticrack.de/decompilers.htm) - 36k - [Cached](#) - [Similar pages](#)

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### PE Explorer: Win32 Delphi reverse engineer EXE, DLL, OCX ...

... the compiler has been identified, the **disassembler** searches the ... If a **relocation** table is found, the ... the addresses to positively identified instructions and the ...  
[www.heaventools.com/PE\\_Explorer\\_disassembler.htm](http://www.heaventools.com/PE_Explorer_disassembler.htm) - 13k - [Cached](#) - [Similar pages](#)

### Manpage of OBJDUMP

... instructions by using the switch --disassembler-options=force ... and booke64 select disassembly of Booke instructions. ... r --reloc Print the **relocation** entries of ...  
[stephane.carrez.free.fr/doc/objdump.html](http://stephane.carrez.free.fr/doc/objdump.html) - 18k - [Cached](#) - [Similar pages](#)

### MTech Thesis Abstract - Y011116

... Generic **Disassembler** facilitates disassembly of programs in a ... For identifying the instructions, depth first search ... is achieved by using **relocation** and symbol ...  
[www.cse.iitk.ac.in/research/mtech2000/Y011116.html](http://www.cse.iitk.ac.in/research/mtech2000/Y011116.html) - 3k - [Cached](#) - [Similar pages](#)

### objdump - Linux Command - Unix Command

... -no-show-raw-insn When disassembling instructions, do not ... including the symbol table and **relocation** entries ... This option directs the **disassembler** to disassemble ...  
[linux.about.com/library/cmd/blcmdl1\\_objdump.htm](http://linux.about.com/library/cmd/blcmdl1_objdump.htm) - 32k - [Cached](#) - [Similar pages](#)

### Disassembling

... A list of instructions that affect control flow is created and ... is v1.09, which provides support for **relocation** and is ... disasm32 Visual Symbolic **Disassembler**. ...  
[www.itee.uq.edu.au/~csmweb/decompilation/disasm.html](http://www.itee.uq.edu.au/~csmweb/decompilation/disasm.html) - 16k - [Cached](#) - [Similar pages](#)

### UNIX man pages : objdump (1)

... when in AT&T mode, instructs the **disassembler** to print ... the printing of register names in disassembled instructions. ... r --reloc Print the **relocation** entries of ...  
[unixhelp.ed.ac.uk/CGI/man-cgi?objdump+1](http://unixhelp.ed.ac.uk/CGI/man-cgi?objdump+1) - 15k - [Cached](#) - [Similar pages](#)

### DataRescue IDA Pro Disassembler: What's new Page

... was incorrectly demangled BUGFIX: amd64 elf **relocation** R\_X86\_64\_PC8 wasn't ... bset and other bit manipulation instructions were not ... is the IDA Pro v4.6 **Disassembler**. ...  
[www.datarescue.com/idabase/idanew451.htm](http://www.datarescue.com/idabase/idanew451.htm) - 44k - [Cached](#) - [Similar pages](#)

GNU Binary Utilities: objdump

... to interpret all **instructions** as THUMB instructions by using the switch '--  
**disassembler-options=force-thumb**'. ... r --reloc Print the **relocation** entries of ...  
[www.linuxselfhelp.com/gnu/binutils/html\\_chapter/binutils\\_4.html](http://www.linuxselfhelp.com/gnu/binutils/html_chapter/binutils_4.html) - 22k - [Cached](#) -  
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Memory Forwarding: Enabling Aggressive Data Layout.. - Mowry, Luk (1998) (Correct)

Optimizations by Guaranteeing the Safety of Data Relocation Todd C. Mowry Chi-Keung Luk y October 1998

data **relocation** (or simply **relocation**) Relocation-based techniques can optimize data layout based on  
[www.cs.cmu.edu/~luk/luk\\_papers/forwarding\\_TR.ps.gz](http://www.cs.cmu.edu/~luk/luk_papers/forwarding_TR.ps.gz)

Memory Forwarding: Enabling Aggressive Layout Optimizations by .. - Chi-Keung Luk (1999) (Correct)  
 (8 citations)

Optimizations by Guaranteeing the Safety of Data Relocation Chi-Keung Luk Todd C. Mowry Department of  
 exist in the address space. Therefore, software-based techniques for improving cache performance  
[www.cs.cmu.edu/~tcm/tcm\\_papers/forwarding\\_isca99.ps.gz](http://www.cs.cmu.edu/~tcm/tcm_papers/forwarding_isca99.ps.gz)

From Programs to Object Code and back again using Logic.. - Bowen (1993) (Correct) (6 citations)

The above approach is the one adopted on the ESPRIT Basic Research ProCoS project (Bjerner, 1992, Bjerner  
 again using Logic Programming: Compilation and **Decompilation** Jonathan Bowen Oxford University  
 Computing

again using Logic Programming: Compilation and **Decompilation** Summary A compiler may be specified by  
 a

<ftp://comlab.ox.ac.uk/pub/Documents/techpapers/Jonathan.Bowen/jsm.ps>

Of Graphs and Coffi Grounds: Decompiling Java - Lam (1998) (Correct)

3) These classes contain methods and data. Our basic unit of interest is the method, which is a list  
 to the original text possible. This is called **decompilation**. Given a class file, one can build an  
 possibility and impossibility which show that **decompilation** is always possible if the program may be  
[www.sable.mcgill.ca/publications/.sable-tr-1998-6.ps.gz](http://www.sable.mcgill.ca/publications/.sable-tr-1998-6.ps.gz)

An Environment for the Reverse Engineering of Executable Programs - Cifuentes (1995) (Correct)

and the entry-point to the program) the **relocation** table, the code segment and the data segment.  
 worked on the automatic generation of decompilers based on compiler specifications using logic and  
 executable code: a reverse compilation or **decompilation** environment that aids in the understanding of  
[www.it.uq.edu.au/~cristina/apsec95.ps](http://www.it.uq.edu.au/~cristina/apsec95.ps)

Partial Automation of an Integrated Reverse Engineering.. - Cristina Cifuentes (1996) (Correct) (3 citations)

determine where the instructions are, where the **relocation** information is, where the symbol table is (if  
 specifications or graphical representations based on high-level source code or design

As previously reported by the author [8] the **decompilation** analysis can be done in a  
[www.cs.uq.edu.au/~cristina/wcre96.ps](http://www.cs.uq.edu.au/~cristina/wcre96.ps)

A Methodology for Decompilation - Cifuentes, Gough (1993) (Correct) (1 citation)

it, relocating addresses specified in the **relocation** table, and setting up the initial contents of  
 flow graph (cfg) generator constructs a cfg of basic blocks 2 for each procedure. A cfg is a  
 A Methodology for **Decompilation** Cristina Cifuentes K. John Gough  
[www.dstc.qut.edu.au/~gough/decomp-method.ps](http://www.dstc.qut.edu.au/~gough/decomp-method.ps)

Comparing Type-Based and Proof-Directed Decompilation - Mycroft, Ohori, Katsumata (2001) (Correct)

Comparing Type-Based and Proof-Directed **Decompilation** Alan Mycroft

Comparing Type-Based and Proof-Directed **Decompilation** Alan Mycroft Computer Laboratory, University  
 In the past couple of years interest in **decompilation** has widened from its initial concentration on

[www.cl.cam.ac.uk/~am/papers/decomp01.ps.gz](http://www.cl.cam.ac.uk/~am/papers/decomp01.ps.gz)

Efficient Support for P-HTTP in Cluster-Based Web Servers - Aron, Druschel, Zwaenepoel (1999) (Correct)  
(9 citations)

Efficient Support for P-HTTP in Cluster-Based Web Servers Mohit Aron Peter Druschel Willy  
[www.cs.rice.edu/~aron/papers/phhttp-lard.ps](http://www.cs.rice.edu/~aron/papers/phhttp-lard.ps)

SemQuery: Semantic Clustering and Querying on.. - Sheikholeslami.. (1998) (Correct)

USA Abstract The effectiveness of the content-based image retrieval can be enhanced using the  
[www.rit.edu/~wcceec/.papers/tkde-semantic.ps](http://www.rit.edu/~wcceec/.papers/tkde-semantic.ps)

Reverse Compilation Techniques - Cifuentes (1994) (Correct) (7 citations)

In general, source binary programs have a **relocation** table of addresses that are to be relocated are presented in this thesis. These techniques are **based** on compiler and optimization theory, and are  
[ftp.it.uq.edu.au/pub/CSM/dcc/decompilation\\_thesis.ps.gz](http://ftp.it.uq.edu.au/pub/CSM/dcc/decompilation_thesis.ps.gz)

The Case For Reliable Concurrent Multicasting Using.. - Levine, Lavo.. (1996) (Correct) (32 citations)  
reliable multicasting over the Internet should be **based** on reliable multicast protocols **based** on a shared  
[www.cse.ucsc.edu/research/ccrg/publications/brian.mm96.ps.gz](http://www.cse.ucsc.edu/research/ccrg/publications/brian.mm96.ps.gz)

Type-Based Decompilation - Mycroft (1999) (Correct) (1 citation)

names, we can also recover information from a **relocation** or symbol table (e.g. in a ELF executable) or  
Type-Based Decompilation or Program Reconstruction via  
Type-Based Decompilation or Program Reconstruction via Type  
[www.cl.cam.ac.uk/users/am/papers/decomp.ps.gz](http://www.cl.cam.ac.uk/users/am/papers/decomp.ps.gz)

Generating Decompilers - Breuer, Bowen (1998) (Correct)

in [37] for a more general readership. Tool-based interactive **decompilation** of low level code to a  
This paper presents an approach for the **decompilation** of object code back to source code using a  
code using a decompiler generator. An example **decompilation** is presented. Potential applications include  
[ftp.cs.reading.ac.uk/pub/formal/jpb/decomp-tr.ps.Z](http://ftp.cs.reading.ac.uk/pub/formal/jpb/decomp-tr.ps.Z)

Configuration Relocation and Defragmentation for.. - Compton, Cooley.. (2000) (Correct)

Configuration **Relocation** and Defragmentation for Reconfigurable  
The configuration overhead of a serial or even a **basic** partially reconfigurable FPGA might outweigh  
[www.ee.washington.edu/faculty/hauck/.publications/Relocate.pdf](http://www.ee.washington.edu/faculty/hauck/.publications/Relocate.pdf)

Performance Comparison Of Video Transport Over ATM.. - Hossain, Kang, Horst (Correct)

All the experimental runs reported in this work are **based** on fully functional Video Server (VS) and Video  
berserk.vlsi.uiuc.edu/people/ashfaq/ieee.mm97.ps

Automatically Learning to Use Discourse Information For.. - Rose, Qu (1996) (Correct)

we discuss how we apply predictions from our plan-based discourse processor discussed in (Rose et al.  
[www.cs.cmu.edu/afs/cs/user/yqu/www/papers/rose-qu-95.ps](http://www.cs.cmu.edu/afs/cs/user/yqu/www/papers/rose-qu-95.ps)

Data Relocation and Prefetching for Programs with Large.. - Yamada, Gyllenhaal.. (1994) (Correct)  
(26 citations)

Data **Relocation** and Prefetching for Programs with Large Data  
all three cache performance problems for array-based applications, rather than a solving these  
[www.crhc.uiuc.edu/IMPACT/ftp/conference/micro-94-relocation.ps](http://www.crhc.uiuc.edu/IMPACT/ftp/conference/micro-94-relocation.ps)

The Impact of Copyright on the Development of Cutting Edge.. - Cifuentes (Correct)

extent of copyright protection on computer software **based** on current legislation in the US and the European  
reverse engineering technologies: emulation, **decompilation**, and binary translation, and present their  
into account. Keywords: intellectual property, **decompilation**, emulation, binary translation. 1  
[www.csee.uq.edu.au/~cristina/wcre992.ps](http://www.csee.uq.edu.au/~cristina/wcre992.ps)

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